

英文求职信模板13电子工程师 PDF转换可能丢失图片或格式  
，建议阅读原文

[https://www.100test.com/kao\\_ti2020/462/2021\\_2022\\_\\_E8\\_8B\\_B1\\_E6\\_96\\_87\\_E6\\_B1\\_82\\_E8\\_c96\\_462312.htm](https://www.100test.com/kao_ti2020/462/2021_2022__E8_8B_B1_E6_96_87_E6_B1_82_E8_c96_462312.htm) 英文简历模板：电子

工程师 ELECTRONICS ENGINEER(Experienced)Sandy Lin  
15/F,TOWER2 ,BRIGHT CHINA,BUILDING1,BEIJING.

**BACKGROUND SUMMARY**Over eleven years of extensive computer/electronics experience.Versed in both digital and analog electronics withspecific emphasis on computer hardware/software.Special expertise in system and component evaluation.Network supervisor responsible for installing/maintaining Arcnet LAN system.Proficient in assembly and C programming languages.Excelent communication skills including written,verbal and interpersonal.**PROFESSIONAL WORK**

**EXPERIENCE**Stevenson Data Systems,Los Angeles,CA

1981-1993Components Evaluation Engineer 1992-1993Responsible for the characterization and evaluation of, and approved vendors list for:Power supplies,oscillators,crystals,and programmable logic used in desktop and laptop computers.Evaluated and recommended quality components that increased product profitability.Created adn developed power supply test plan used for evqluating third party power supplies.Interacted with vendors to resolve problems associated with components qualification.Technical advisor for Purchasing.Promoted to engineer II.Design Evaluation Engineer 1990-1992Evaluated new computer product designs ,solving environmental problems on prototype computers.Conducted

systems analysis on new computer products to ensure hardware, software and mechanical design integrity. Designed hardware and software for PC, ISA bus programmable load board used for environmental testing. Performed reliability life testing on computer systems. Installed/maintained 20 user. Novell, Arcnet LAN system. Examined system and sub-system susceptibility to electrostatic discharge in order to meet IEC-801-2 industry standards. Analyzed complete power and load of computer system and subsystem to verify power and load estimations. Assistant Engineer 1981-1990 Performed extensive hardware evaluation on prototype computers, tested prototype units for timing violation using the latest state-of-the-art test equipment, digital oscilloscopes and logic analyzers. Performed environmental, ESD and acoustic testing. Designed and built a power-up test used to test prototype computers during cold boot. EDUCATION Bachelor of Science in Electrical Engineering -- University of Southern California 1990 Associate in Engineering Electronics -- University of Southern California 1981 Job descriptions emphasize candidates accomplishments. Work history is stated in reverse chronological order, with most recent employment listed first. 100Test 下载频道开通，各类考试题目直接下载。详细请访问 [www.100test.com](http://www.100test.com)