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[https://www.100test.com/kao\\_ti2020/645/2021\\_2022\\_\\_E5\\_9B\\_BD\\_E9\\_99\\_85\\_E5\\_95\\_86\\_E5\\_c29\\_645178.htm](https://www.100test.com/kao_ti2020/645/2021_2022__E5_9B_BD_E9_99_85_E5_95_86_E5_c29_645178.htm) id="koke" class="zizi">

3dnow!(3d no waiting) alu(arithmetic logic unit, 算术逻辑单元) agu(address generation units, 地址生成单元) bga(ball grid array, 球状矩阵排列) bht(branch prediction table, 分支预测表) bpu(branch processing unit, 分支处理单元) brach pediction(分支预测) cmos: complementary metal oxide semiconductor, 互补金属氧化物半导体 cisc(complex instruction set computing, 复杂指令集计算机) clk(clock cycle, 时钟周期) cob(cache on board, 板上集成缓存) cod(cache on die, 芯片内集成缓存) cpga(ceramic pin grid array, 陶瓷针型栅格阵列) cpu(center processing unit, 中央处理器) data forwarding(数据前送) decode(指令解码) dib(dual independent bus, 双独立总线) ec(embedded controller, 嵌入式控制器) embedded chips(嵌入式) epic(explicitly parallel instruction code, 并行指令代码) fadd(floationg point addition, 浮点加) fcpga(flip chip pin grid array, 反转芯片引脚栅格阵列) fdiv(floationg point divide, 浮点除) femms: fast entry/exit multimedia state, 快速进入/退出多媒体状态 fft(fast fourier transform, 快速热欧姆转换) fid(fid: frequency identify, 频率鉴别号码) fifo(first input first output, 先入先出队列) flip-chip(芯片反转) flop(floationg point operations per second, 浮点操作/秒) fmul(floationg point multiplication, 浮点乘) fpu(float point unit, 浮点运算单元) fsub(floationg point subtraction, 浮点减) gvpp(generic visual perception processor,

常规视觉处理器) hl-pbga: 表面黏著, 高耐热、轻薄型塑胶球状矩阵封装 ia(intel architecture, 英特尔架构) icu(instruction control unit, 指令控制单元) id: identify, 鉴别号码 idf(intel developer forum, 英特尔开发者论坛) ieu(integer execution units, 整数执行单元) imm:intel mobile module, 英特尔移动模块 instructions cache, 指令缓存 instruction coloring(指令分类) ipc(instructions per clock cycle, 指令/时钟周期) isa(instruction set architecture, 指令集架构) kni(katmai new instructions, katmai新指令集, 即sse) latency(潜伏期) ldt(lightning data transport, 闪电数据传输总线) local interconnect(局域互连) mesi(modified, exclusive, shared, invalid: 修改、排除、共享、废弃) mmx(multimedia extensions, 多媒体扩展指令集) mmu(multimedia unit, 多媒体单元) mflops(million floating point/second, 每秒百万个浮点操作) mhz(million hertz, 兆赫兹) mp(multi-processing, 多重处理器架构) mps(multiprocessor specification, 多重处理器规范) msrs(model-specific registers, 特别模块寄存器) naoc(no-account overclock, 无效超频) ni: non - intel, 非英特尔 olga(organic land grid array, 基板栅格阵列) ooo(out of order, 乱序执行) pga: pin-grid array(引脚网格阵列), 耗电大 post-risc pr(performance rate, 性能比率) psn(processor serial numbers, 处理器序列号) pib(processor in a box, 盒装处理器) ppga(plastic pin grid array, 塑胶针状矩阵封装) pqfp(plastic quad flat package, 塑料方块平面封装) raw(read after write, 写后读) register contention(抢占寄存器) register pressure(寄存器不足) register renaming(寄存器重命名) remark(芯片频率重标识) resource contention(资源冲突) retirement(指

令引退) risc(reduced instruction set computing , 精简指令集计算机) sec: single edge connector , 单边连接器 shallow-trench isolation(浅槽隔离) simd(single instruction multiple data , 单指令多数据流) sio2f(fluorided silicon oxide , 二氧氟化硅) smi(system management interrupt , 系统管理中断) smm(system management mode , 系统管理模式) smp(symmetrical multi-processing , 对称式多重处理架构) soi: silicon-on-insulator , 绝缘体硅片 sonc(system on a chip , 系统集成芯片) spec(system performance evaluation corporation , 系统性能评估测试) sqrt(square root calculations , 平方根计算) sse(streaming simd extensions , 单一指令多数据流扩展) superscalar(超标量体系结构) tcp: tape carrier package(薄膜封装) , 发热小 throughput(吞吐量) tlb(translate look side buffers , 翻译旁视缓冲器) uswc(uncacheabled speculative write combination , 无缓冲随机联合写操作) valu(vector arithmetic logic unit , 向量算术逻辑单元) vliw(very long instruction word , 超长指令字) vpu(vector permutate unit , 向量排列单元) vpu(vector processing units , 向量处理单元 , 即处理mmx、sse等simd指令的地方) 把国际商务师站点加入收藏夹 欢迎进入 : 2009年国际商务师课程免费试听 更多信息请访问 : 百考试题论坛国际商务师 100Test 下载频道开通 , 各类考试题目直接下载。详细请访问 [www.100test.com](http://www.100test.com)